

**Discrete DC Output Modules**

- Modular I/O
- Fieldbus Independent Configuration
- IP 20 Protection
- Various I/O Styles



- BL20-2DO-24VDC-2A-P
- BL20-2DO-24VDC-0.5A-N
- BL20-2DO-24VDC-0.5A-P
- BL20-4DO-24VDC-0.5A-P



**Electrical**

- Operating Current: <math>< 33 \text{ mA}</math> from  $V_{MB}$   
<math>< 25 \text{ mA}</math> from  $V_{IO}$  (...-0.5A...)  
<math>< 50 \text{ mA}</math> from  $V_{IO}$  (...-2A...)
- Output Current: see table on facing page (from  $V_{IO}$ )

**Power Distribution**

- Outputs:  $V_{IO}$
- Logic:  $V_{MB}$  and  $V_{IO}$

**Mechanical**

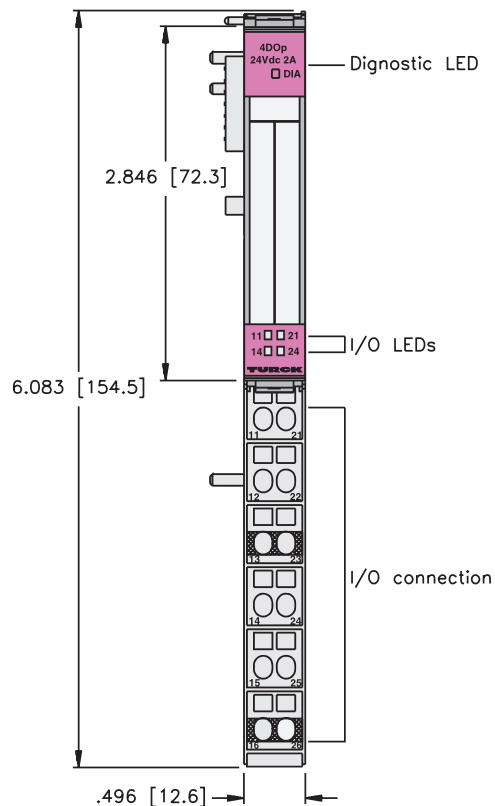
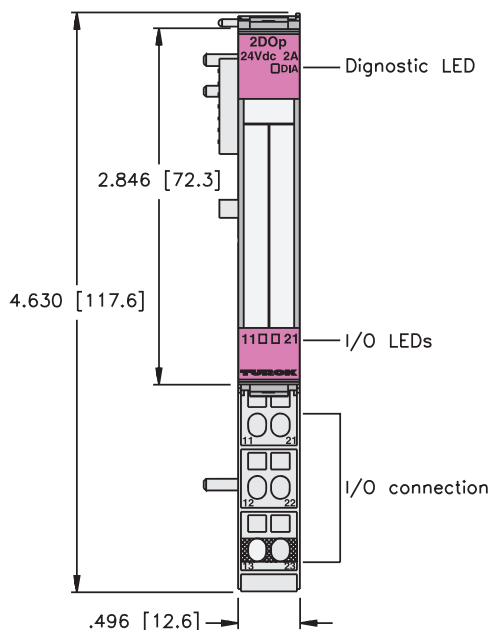
- Operating Temperature: 0 to +55°C (+32 to +131°F)
- Protection: IP 20

**Diagnostics (Logical)**

- Diagnostic information available through the fieldbus gateway

**Diagnostics (Physical)**

- LED to indicate module bus communication status as well as I/O diagnostics
- LEDs for each I/O point to indicate on/off status





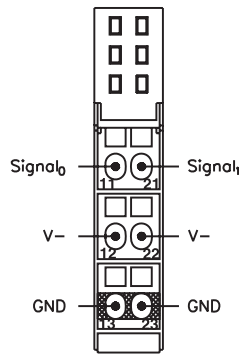
|         |      |
|---------|------|
| Outputs | Data |
|---------|------|

| Part Number                                  | Input Count | Pinout | Current | Individual Diagnostics | Wire-Break Detection | I/O Map |
|--|-------------|--------|---------|------------------------|----------------------|---------|
| BL20-2DO-24VDC-0.5A-P with BL20-S3*-SBC**    | 4           | B20-3B | 0.5 A   |                        |                      | 1       |
| BL20-2DO-24VDC-0.5A-N with BL20-S3*-SBC**    | 4           | B20-3B | 0.5 A   |                        |                      | 1       |
| BL20-2DO-24VDC-2A-P with BL20-S3*-SBC**      | 4           | B20-3B | 2 A     |                        |                      | 1       |
| BL20-4DO-24VDC-0.5A-P with BL20-S6*-SBCSBC** | 4           | B20-6B | 0.5 A   |                        |                      | 2       |

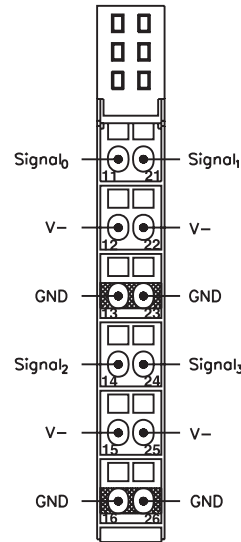
\* T = Tension clamp  
 S = Screw clamp  
 \*\* Base modules sold separately. See pages C56 - C60.

### Output Connectors

**B20-3B**



**B20-6B**



**I/O Data Map 1**

| Out | Byte | Bit 7                           | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|-----|------|---------------------------------|-------|-------|-------|-------|-------|-------|-------|
|     | n-1  | (Data for modules to the left)  |       |       |       |       |       |       |       |
|     | n    | Data for next discrete modules  |       |       |       |       |       | 0-1   | 0-0   |
|     | n+1  | (Data for modules to the right) |       |       |       |       |       |       |       |

**I/O Data Map 2**

| Out | Byte | Bit 7                           | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|-----|------|---------------------------------|-------|-------|-------|-------|-------|-------|-------|
|     | n-1  | (Data for modules to the left)  |       |       |       |       |       |       |       |
|     | n    | Data for next discrete modules  |       |       |       | 0-3   | 0-2   | 0-1   | 0-0   |
|     | n+1  | (Data for modules to the right) |       |       |       |       |       |       |       |